

VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

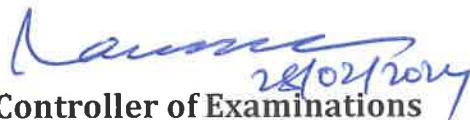
Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 500 090, Telangana, India

TIMETABLE FOR II B.TECH. II SEMESTER (R15), SUPPLEMENTARY EXAMINATION – APRIL, 2024

| Branch | 19 th April 2024 10:00 A.M. to 1:00 P.M. | 22 nd April 2024 10:00 A.M. to 1:00 P.M. | 24 th April 2024 10:00 A.M. to 1:00 P.M. | 26 th April 2024 10:00 A.M. to 1:00 P.M. | 29 th April 2024 10:00 A.M. to 1:00 P.M. | 1 st May 2024 10:00 A.M. to 1:00 P.M. |
|--------|--|---|---|---|---|---|
| | Friday | Monday | Wednesday | Friday | Monday | Wednesday |
| CE | Concrete Technology (5CE10) | Mechanical and Electrical Technology (5ME20) | Hydraulics and Hydraulic Machines (5CE08) | Strength of Materials – II (5CE09) | Computational Methods (5BS13) [Common To-AE, CE, ME] | Structural Analysis – I (5CE11) |
| EEE | Electrical Machines – II (5EE05) | Electronic Circuits (5EC16) | Business Economics and Financial Analysis (5BS41) [Common To-EEE, ME] | Power Systems-I (5EE06) | Fluid Mechanics and Hydraulic Machines (5ME25) | -- |
| ME | Thermal Engineering (5ME06) | Kinematics of Machinery (5ME05) | Business Economics and Financial Analysis (5BS41) [Common To-EEE, ME] | Basic Electrical and Electronics Engineering (5EE23) [Common To-AE, ME] | Computational Methods (5BS13) [Common To-AE, CE, ME] | -- |
| ECE | Analog Communications (5EC06) | Switching Theory and Logic Design (5EC03) [Common To-ECE, EIE] | Control Systems (5EE08) [Common To-ECE, EIE] | Pulse and Digital Circuits (5EI04) [Common To-ECE, EIE] | Electronic Circuit Analysis (5EC04) [Common To-ECE, EIE] | Electromagnetic Theory and Transmission Lines (5EC05) |
| CSE | Computer Organization (5IT04) [Common To-CSE, IT] | Data Base Management Systems (5CS05) [Common To-CSE, IT] | JAVA Programming (5CS15) [Common To-CSE, IT] | Design and Analysis of Algorithms (5CS06) [Common To-CSE, IT] | Formal Languages and Automata Theory (5CS04) [Common To-CSE, IT] | Software Engineering (5CS07) [Common To-CSE, IT] |
| EIE | Signals And Systems (5EI03) | Switching Theory and Logic Design (5EC03) [Common To-ECE, EIE] | Control Systems (5EE08) [Common To-ECE, EIE] | Pulse and Digital Circuits (5EI04) [Common To-ECE, EIE] | Electronic Circuit Analysis (5EC04) [Common To-ECE, EIE] | |
| IT | Computer Organization (5IT04) [Common To-CSE, IT] | Data Base Management Systems (5CS05) [Common To-CSE, IT] | JAVA Programming (5CS15) [Common To-CSE, IT] | Design and Analysis of Algorithms (5CS06) [Common To-CSE, IT] | Formal Languages and Automata Theory (5CS04) [Common To-CSE, IT] | Software Engineering (5CS07) [Common To-CSE, IT] |

Narayan
28-02-2024

| Branch | 19 th April 2024 10:00 A.M. to 1:00 P.M. | 22 nd April 2024 10:00 A.M. to 1:00 P.M. | 24 th April 2024 10:00 A.M. to 1:00 P.M. | 26 th April 2024 10:00 A.M. to 1:00 P.M. | 29 th April 2024 10:00 A.M. to 1:00 P.M. | 1 st May 2024 10:00 A.M. to 1:00 P.M. |
|--------|--|--|--|--|---|---|
| | Friday | Monday | Wednesday | Friday | Monday | Wednesday |
| AE | Applied Thermodynamics (5AE01) | Automotive Engines and Systems (5AE02) | Theory of Machines (5AE03) | Basic Electrical and Electronics Engineering (5EE23) [Common To-AE, ME] | Computational Methods (5BS13) [Common To-AE, CE, ME] | -- |


 Controller of Examinations

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Note : 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY
 2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.